

**Notice of References Cited**

Application/Control No.

10/072,798

Applicant(s)/Patent Under  
Reexamination  
TSAI ET AL.

Examiner

DuyVu n Deo

Art Unit

1765

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,804,088	09-1998	McKee, Jeffrey Alan	216/47
	B	US-4,269,654	05-1981	Deckert et al.	438/756
	C	US-6,420,097	07-2002	Pike et al.	430/313
	D	US-6,498,061	12-2002	Divakaruni et al.	438/243
	E	US-5,431,770	07-1995	Lee et al.	438/592
	F	US-6,036,875	03-2000	Lin, Ming-Ren	216/51
	G	US-6,362,111	03-2002	Laaksonen et al.	438/725
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, Vol. 1, 1986, pages 529, 539.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.